ANALYSIS OF SIGNAL TRANSITION ACTIVITY IN FIR FILTERS IMPLEMENTED BY PARALLEL MULTIPLIER ACCUMULATOR BASED ON MODIFIED BOOTH ALGORITHM

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ABSTRACT

The MAC architecture is used in real time digital signal processing and multimedia information processing which requires high throughput. A novel method to estimate the transition activity at the nodes of a multiplier accumulator architecture based on modified booth algorithm implementing finite impulse response filter is proposed in this paper. The input signals are described by a stationary Gaussian process and the transition activity per bit of a signal word is modeled according to the dual bit type (DBT) model. This estimation is based on the mathematical formulation by multiplexing mechanism on the breakpoints of the DBT model.

KEYWORDS

Digital Signal Processing (DSP), MAC, dual-bit type (DBT), multiplexing mechanism, signal transition activity, word level activity

1. INTRODUCTION

Multiplier and multiplier-and-accumulator (MAC) [1] are the important elements of the digital signal processors. For example, many filters and channel estimators require FIR or FFT/IFFT computations that MAC units can accelerate efficiently. The Booth multiplier has the highest operational speed and less hardware count.

To increase the speed of MAC, the modified radix-4 Booth's algorithm (MBA) [2] is usually used. The power dissipation in very large scale integration (VLSI) is the major concern. The dynamic power consumed during charging and discharging the load and parasitic capacitances, depends on the number of transitions occurring at the capacitive nodes, estimation of the average

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number of bit level signal transitions is a most requirement of the power estimation techniques. Estimation of the data path power consumption at the architectural level based on the dual –bit-type (DBT) model has been explained in [3]. In DBT model, the LSB's of a word behaves as random sequences and the MSB's exhibits nonrandom behavior. The bit level activity is explained in [4]. The DBT model and high level signal statistics for the estimation of the bit level transition activity on filters has been proposed in [5]. This paper deals with the accurate calculation of transition activity at the nodes of a MAC which is used in FIR filter, is discussed. The DBT model is followed to calculate required word-level signal statistics at the nodes of the MAC, with known statistics.

2. MULTIPLIER ARCHITECHTURE

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Fig.1. Basic arithmetic operation of multiplication and accumulation

The Multipliers has two parts. The first part is to generate the partial products, and the second one is to collect and add them. The multiplier accumulator composed of three blocks: the booth encoder, partial product summer and accumulator, and final adder. The partial product can be generated by using any one of the multiplication algorithm which uses bit serial, serial-parallel, or full parallel techniques. The booth algorithm or the modified booth algorithm is used for the generation of partial products. The basic arithmetic operation of multiplication and accumulation is shown in fig.1. The signed multiplication based on 2's complement is possible. If N-bit data

are multiplied the number of partial product also equal to N. the Modified Booth encoding is used to reduce the number of partial products [7].



Output

Fig.2. Multiplication process by Modified Booth Encoding.

3. WORD LEVEL STATISTICS AND TRANSITION ACTIVITY

The input sequence of a DSP system bex(n) and can be modeled by stationary Gaussian process with mean value μ variance σ^2 , and correlation factor ρ . Let $b_{<i>}(n)$, be the ith bit of the binary word representing x(n). The probability of $b_{<i>}(n)$ be one or zero and is denoted by p_1^i and p_0^i respectively, the conditional probability of $b_{<i>}(n)$ be in state *l* is given by $b_{<i>}(n-1)$, in state k is denoted by p_{kl}^i , where k, l = 0,1. The transition probability tp_{kl}^i is the probability of $b_{<i>}(n) = 1$ and $b_{<i>}(n) = k$. Therefore $p_{kl}^i = p_k^i p_{kl}^i$. Thus, the transition activity t_i of $b_{<i>}(n)$ is

 $t_i = tp_{01}^i + tp_{10}^i = p_0^i p_{01}^i + p_1^i p_{10}^i$ It is observed that for $p_0^i = p_1^i = 0.5, p_{01}^i = p_{10}^i$ and therefore $t_i = p_{01}^i$

The transition activity is a function of bit level correlation factor $\rho_{<i>}$ [5].

 $t_i = 2p_1^i p_0^i (1 - \rho_{<i>})$

4. BIT LEVEL STATISTICS

The bit level correlation factor is modeled by DBT model [5],[3]. A signal word is divided into three regions of continuous bits as the LSB, linear and MSB regions. The breakpoints BP_0 and BP_1 which defines the linear regions given by

$$BP_0 = \log_2^{\sigma} + \log_2^{\left[\sqrt{1-\rho^2} + \frac{|\rho|}{8}\right]}$$
$$BP_0 = \log_2^{3\sigma}$$

The MSB's are highly correlated signals which have low transition activity while anticorrelated ones possess high activity. The transition activity can be expressed as

$$t_{i} = \begin{cases} 2p_{1}^{i}p_{0}^{i}, & i < BP_{0} \\ \frac{t_{msb}(i-BP_{0})-2p_{1}^{i}p_{0}^{i}(i-BP_{1})}{BP_{1}-BP_{0}}, & BP_{0} \le i \le BP_{1} \\ t_{msb}, & i \ge BP_{1} \end{cases}$$

Where t_{msb} is the activity of the MSB. For zero mean Gaussian signals, the word level correlation factor for t_{msb} is given by

$$t_{msb} = (1/\pi) cos^{-1}(\rho)$$

5. SIGNAL STATISTICS IN FIR FILTER

The signal statistics at the output of the l th tap of an FIR filter is the function of the input statistics and the filter coefficients are expressed as

$$\begin{split} \mu_{f-l} &= \mu \sum_{i=0}^{l} c_i \\ \sigma_{f-l}^2 &= \sigma^2 \sum_{i=0}^{l} c_i^2 + 2 \sum_{i=0}^{l-1} \sum_{i=0}^{l} \rho(j-i) c_i c_j \\ \rho_{f-l} &= \frac{\sigma^2}{\sigma_{f-l}^2} \left(\sum_{i=0}^{l-1} c_i c_{i+1} + \sum_{i=0}^{l} \sum_{j=0}^{l} c_i c_j \rho(j-i+1) \right. \\ &+ \sum_{i=0}^{l-2} \sum_{j=i+2}^{l} c_i c_j \rho(j-i-1) \right) \end{split}$$

The structure of FIR filter is shown in fig.3.



Fig.3.FIR filter structure

6. MAPPING THE FIR FILTER STRUCTURE ON A MAC BASED ON MODIFIED BOOTH ALGORITHM

The architecture of FIR filter implemented by MAC using modified Booth algorithm is shown in Fig.4. Registers are inserted at the input and output of the MAC unit to increase the data throughput rate. The sampling period of the input signal at the nodes of the filter structure is a sequence in the time domain at the corresponding nodes of the MAC architecture. During the nth input signal sampling period, the sequence [x(n), x(n-1), x(n-2)...x(n-k+1)] appears at the input node #1 of the MAC. The control signal is given at node #2, is a increasing integer in the interval [0, k-1], returning back to zero at every new sample. A mathematical approach to find the statistics of its input sequence is mixing method [8]. The output distribution is a linear combination of the inputs. When all the inputs show identical normal distribution the output distribution remains Gaussian. When all the input sequences have equal mean values and different variances the output distribution is symmetric around the mean value, but not normal. When the input sequences have different mean values, the output distribution is nonsymmetric.



Fig.4.Signal activity at the nodes of MAC unit with Modified Booth Algorithm

7. SIGNAL ACTIVITY AT THE NODES OF MAC

The input signals at the nodes of the MAC unit are the output from the multiplexers. The multiplexing mechanism is used for the signal statistics. The input signal x(n) is described by a stationary Gaussian process with signal statistics μ , σ^2 and ρ . The statistics of the input signal at node#1 are given by

$$\begin{split} \mu_{1} &= \mu \\ \sigma_{1}^{2} &= \sigma^{2} \\ \rho_{1} &= (E[y_{1}(n)y_{1}(n-1)] - \mu_{1}^{2}/\sigma_{1}^{2} \end{split}$$

Where

$$\begin{split} E[y_1(n)y_1(n-1)] \\ = \frac{1}{k} \Biggl(\sum_{i=2}^k E[x(n-i+1)x(n-i+2)] \\ &+ E[x(n+1)x(n-k+1)] \Biggr) \end{split}$$

Since the input sequence is assumed to be stationary

$$E[x(n-1)x(n)] = \cdots$$

= $E[x(n-k+1)x(n-k+2)]$
= $\rho\sigma^2 + \mu^2$
 $E[x(n+1)x(n-k+1)] = \rho(k)\sigma^2 + \mu^2$
 $\approx \rho^k \sigma^2 + \mu^2$

The correlation coefficient of $y_1(n)$ is $\rho_1 = \frac{((k-1)\rho + \rho^k)}{k}$

Since $\rho < 1$, for higher order filter the correlation coefficient at node#1 approximates the correlation of the input signal.

The statistics of the sequence of the coefficients at node#2 is

$$\mu_2 = \frac{1}{k} \sum_{i=0}^{k-1} c_i$$

$$\sigma_2^2 = \frac{1}{k} \sum_{i=0}^{k-1} c_i^2 - \mu_2^2$$

$$\rho_2 = \frac{1}{k\sigma_2^2} \left(\left(\sum_{i=0}^{k-2} c_{i+1}c_i \right) + c_0 c_{k-1} - k\mu_2^2 \right)$$

The statistics of the signal sequence at the output of the multiplier at node#3 is

$$\begin{split} \mu_3 &= \mu_2 \mu_1 \\ \sigma_3^2 &= (\sigma_2^2 + \mu_2^2)(\sigma_1^2 + \mu_1^2) - \mu_3^2 \\ \rho_3 &= \left((\rho_2 \sigma_2^2 + \mu_2^2)(\rho_1 \sigma_1^2 + \mu_1^2) - \mu_3^2 \right) / \sigma_3^2 \end{split}$$

The sequence at the output nodes #2k - #(3k-1) of the filter are given by

$$\begin{split} \mu_4 &= \frac{\mu}{k} \sum_{j=0}^{k-1} \sum_{i=0}^{j} c_i \\ \sigma_4^2 &= \frac{1}{k} \sum_{i=0}^{k-1} [\sigma_{f-i}^2 + \mu_{f-i}^2] - \mu_4^2 \\ \rho_4 &= \frac{1}{k \sigma_4^2} \left(\sum_{i=0}^{k-2} [\sigma_{f-i}^2 + \mu_{f-i}^2] \\ &+ \sum_{i=1}^{k-1} \sum_{j=0}^{i-1} c_i c_j [\rho^{[j-i]} \sigma^2 + \mu^2] \\ &+ c_0 \left[\sigma^2 \sum_{i=0}^{k-1} c_i \rho^{i+1} + \mu^2 \sum_{i=0}^{k-1} c_i \right] \\ &- k \mu_4^2 \end{split}$$

Where μ_{f-i} and σ_{f-i}^2 are given by (6). To verify real life signals like music and speech signals the proposed models can be used.

8. CONCLUSION

In this paper the method of estimating the Signal Transition Activity in FIR Filters implemented by parallel Multiplier Accumulator based on Modified Booth Algorithm has been proposed. The DBT model is used to analyze the signal transition activity by means of the signal statistics. The mathematical formulation of the multiplexing mechanism is developed for the signal statistics. The mapping of FIR filter structure on a MAC based on modified booth algorithm increases the data throughput rate. The proposed multiplexing mechanism is used for the signal transition activity at the nodes.

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